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10/663,779	09/17/2003 RULE	257	2826	

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**** CONTINUING DATA *******
 This application is a CON of 10/377,785 03/04/2003 PAT 6,674,122
 which is a CON of 09/660,923 09/13/2000 PAT 6,531,735

**** FOREIGN APPLICATIONS *******
 JAPAN 2000-83246 03/21/2000
 JAPAN 11-263154 09/17/1999
 JAPAN 11-263155 09/17/1999

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 68	TOTAL CLAIMS 1	INDEPENDENT CLAIMS 1
Verified and Acknowledged	Examiner's Signature _____ Initials _____				

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TITLE
 Semiconductor integrated circuit having two switch transistors formed between two diffusion-layer lines

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